

INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449)				ATTY. DOCKET NO. 60188-970		SERIAL NO. Not yet assigned <div style="font-size: 1.5em; font-weight: bold;">10/510388</div>	
				APPLICANT Masayuki OZASA, et al.			
				FILING DATE October 07, 2004		GROUP Not yet assigned	

U.S. PATENT DOCUMENTS							
EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
		US					
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FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	CITE NO.	Foreign Patent Document Country Codes - Number - Kind Codes (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Figures Appear	Translation	
						Yes	No
<i>NW</i> ↓ <i>NW</i>		JP 11-500883 A	01/19/1999		Corresponds with PCT International Publication No. WO 97/06595	JAPAN (w/English Translation of Claims)	
		WO 97/06595	02/20/1997	PHILIPS ELECTRONICS N.V.	Corresponds with JP 11-500883	X	
		JP 6-18355 A	01/19/1998	ASAHI KASEI MICRO SYST KK		JAPAN (w/English Abstract)	
		JP 6-18354 A	01/19/1998	ASAHI KASEI MICRO SYST KK		JAPAN (w/English Abstract)	
		JP 6-90125 A	03/29/2004	FUJITSU TEN LTD.			
		JP 6-74322 A	03/18/1997	FUJI PHOTO FILM CO., LTD.			
		JP 11-355086 A	12/24/1999	SONY CORP.			
		JP 6-169227 A	08/14/1994	MITSUBISHI ELECTRIC CORP.			

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)			
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	
<i>NW</i> <i>NW</i>		TAMAI, Tokumichi., "Semiconductor Circuit Design Technology." Nikkei BP, Inc., First Edition, November 16, 1990, pp.308-311 (Including Partial English Translation)	
		GRAY, Paul, R., et al. "Analysis and Design of Analog Integrated Circuits." John Wiley & Sons, Inc. Third Edition. Chapter 3, Single Transistor and Two-Transistor Amplifiers, 1993, pp.244-249	

EXAMINER <div style="font-size: 1.2em; font-weight: bold;">NGUYEN, KY</div>	DATE CONSIDERED <div style="font-size: 1.2em; font-weight: bold;">3/2/06</div>
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